	Application No.	Applicant(s)
Notice of Allowability	10/716,705	AOKI, KASAKI
	Examiner	Art Unit
	Gene N Auduong	2818
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	ars on the cover sheet with OR REMAINS) CLOSED in or other appropriate commu GHTS. This application is si	this application. If not included nication will be mailed in due course. THIS
1. \boxtimes This communication is responsive to <u>application filed Novel</u>	<u>mber 20, 2003</u> .	
2. The allowed claim(s) is/are <u>1-13</u> .		
3. \boxtimes The drawings filed on <u>20 November 2003</u> are accepted by	the Examiner.	
 4. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	been received. been received in Application	n No
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONMI THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requirements
 A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give 		
6. CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftsperso 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying Indicia such as the application number (see 37 CFR 1.6 each sheet. Replacement sheet(s) should be labeled as such in the	on's Patent Drawing Review Amendment / Comment or 84(c)) should be written on th	in the Office action of e drawings in the front (not the back) of
 DEPOSIT OF and/or INFORMATION about the depos attached Examiner's comment regarding REQUIREMENT F 		
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/06 Paper No./Mail Date 11-20-03 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Su Paper No./N 3), 7. ☐ Examiner's A	Gene N Auduong
		Primary Examiner Art Unit 2818

Art Unit: 2818

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on November 20, 2003 is being considered by the examiner.

Allowable Subject Matter

- 3. Claims 1-13 are allowed.
- 4. The following is an examiner's statement of reasons for allowance: The prior art does not teach or fairly disclose a ferroelectric memory comprising a ROM area and a RAM area both including memory cells each having a ferroelectric capacitor, wherein the ROM area comprises: at least one real memory cell for storing data to be written in manufacturing process of the ferroelectric memory; a dummy memory cell; a reference memory cell having a ferroelectric capacitor therein whose residual dielectric polarization value is set to a predetermined value in the manufacturing process; a first bit line connected to the real memory cell and the dummy memory cell, and transferring therethrough an electric charge corresponding to a residual dielectric polarization value of the ferroelectric capacitor which is included in a selected one of the real memory cell and dummy memory cell; a second bit line connected to the reference memory cell, and transferring therethrough an electric charge corresponding to a residual dielectric polarization value of the ferroelectric capacitor which is included in the reference memory cell being selected in synchronization with a selection of the real memory cell or the dummy memory cell; and a sense amplifier connected to the first and second bit lines, for

Application/Control Number: 10/716,705

Art Unit: 2818

amplifying a difference in voltage between the first and second bit lines, the difference occurring due to the transferred electric charges..

Page 3

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gene N Auduong whose telephone number is (571) 272-1773.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

GA

October 07, 2004

Gene N Auduong Primary Examiner Art Unit 2818